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## UNITED STATES PATENT AND TRADEMARK OFFICE

UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND  
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

OFFICIAL

## NOTICE OF DRAWING INCONSISTENCY WITH SPECIFICATION

The drawings filed 5-14-04 have been received. However, an inconsistency exists between the drawings and the Brief Description of the Drawings in the specification.

Figures 6 are listed in the Brief Description of the Drawings in the specification but not contained in the Drawings.

Figures \_\_\_\_\_ are contained in the Drawings but not listed in the Brief Description of the Drawings in the specification.

Applicant is required to correct the above-noted inconsistency within a time period of **ONE MONTH or THIRTY (30) DAYS, whichever is longer**, from the mailing date of this Notice, or within the time remaining in the time period set forth in the Notice of Allowability (Form PTOL-37) to file corrected drawings, whichever is longer. **NO EXTENSION OF THIS TIME PERIOD MAY BE GRANTED UNDER EITHER 37 CFR 1.136 (a) OR (b)**

Failure to correct the above noted inconsistency will result in **abandonment** of the application.

The file will be held in the Publishing Division to await the correction of the inconsistency.

**Return Corrected Drawings/Specification to:**

Mail Stop Issue Fee

~~Commissioner for Patents~~

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FORM PTO-1631 (REV. 10-03)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RE: Mail Step Issue Fee  
Commission for patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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In re Application of: Chou H. Li

Serial No. 09/670,874

Group Art Unit: 2826

Filed: September 27, 2000

Examiner: Victor Mandela

For: Semiconductor Integrated Circuit Device

## CORRECTION OF FIG. 6

Assistant Commissioner of Patents

June 15, 2004

Sir:

In response to your "Notice of Drawing Inconsistency with Specification" dated June 1, 2004 and received June 10, 2004, please amend the specification as follows. This correction is complete and replaces the one faxed you on June 14, 2004.

Page 16, line 22, replace ";" by - - ; and - - ;

Page 17, lines 1-3, replace ";" and Fig. 6 is an atomic ... array of circuit" by - - - - ; and

Page 57, line 23 to page 58, line 1, delete "The field layer 61 in Fig. 6 ... layers".


The above corrections are made to correct the improper listing of Figure 6 in the Brief Description of the Drawings in the specification. The Fig. 6 was not needed in this application.

Any fees necessary to effect entry of this correction should be charged to the undersign's deposit account No. 12-1219. If needed, please immediately contact my authorized attorney, James a. Poulos, III, Reg. No. 31,714, Phone: (301) 588-7013, Fax: (301) 496-6341.

Respectfully submitted,

  
Chou H. Li, Reg. No. 24,925

The undersigned certifies that this correction was filed on by facsimile and transmitted to the USPTO to 703 308-6789 on June 15, 2004.

  
Chou H. Li

Date: June 15, 2004